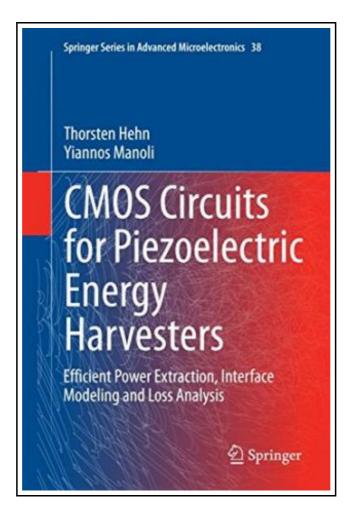
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## CMOS CIRCUITS FOR PIEZOELECTRIC ENERGY HARVESTERS: EFFICIENT POWER EXTRACTION, INTERFACE MODELING AND LOSS ANALYSIS (HARDBACK)

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